

**AMENDMENTS IN THE SPECIFICATION**

*Please replace the paragraph beginning on page 1, line 6 with the following paragraph:*

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The present invention is related to the subject matter of the following commonly assigned, co-pending United States Patent Applications filed concurrently herewith:

Serial No. \_\_\_\_\_ 09/588,608 (Docket No. AT9-99-501) entitled "Multiprocessor Speculation Mechanism Via a Barrier Speculation Flag";

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Serial No. \_\_\_\_\_ 09/588,607 (Docket No. AT9-99-502) entitled "System and Method for Asynchronously Overlapping Storage Barrier Operations With Old and New Storage Operations";

Serial No. \_\_\_\_\_ 09/588,606 (Docket No. AT9-99-503) entitled "Multiprocessor Speculation Mechanism With Imprecise Recycling of Storage Operations";

Serial No. \_\_\_\_\_ 09/588,605 (Docket No. AT9-99-504) entitled "Multiprocessor Speculation Mechanism for efficiently Managing Multiple Barrier Operations";

Serial No. \_\_\_\_\_ 09/588,509 (Docket No. AT9-99-506) entitled "Mechanism for Folding Storage Barrier Operations in a Multiprocessor System";

Serial No. \_\_\_\_\_ 09/588,483 (Docket No. AT9-99-507) entitled "Multi-level Multiprocessor Speculation Mechanism"; and

Serial No. \_\_\_\_\_ 09/588,507 (Docket No. AT9-99-508) entitled "System and Method for Providing Multiprocessor Speculation Within a Speculative Branch Path".

The content of the above-referenced applications is incorporated herein by reference.

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